EECE 277
FPGA Design

Spring 2005, Dr. William H. Robinson

Homework Assignment One

Due: Monday, January 24, 2005 at the beginning of class

Problem I. Problem 2.8, Brown/Vranesic textbook, page 68
Problem II. Problem 2.9, Brown/Vranesic textbook, page 68
Problem III. Problem 2.18, Brown/Vranesic textbook, page 68
Problem IV. Problem 2.28, Brown/Vranesic textbook, page 70
Problem V. Problem 2.30, Brown/Vranesic textbook, page 70
Problem VI. Problem 2.33, Brown/Vranesic textbook, page 70
Problem VII. Problem 2.34, Brown/Vranesic textbook, page 70
Problem VIII. Problem 2.38, Brown/Vranesic textbook, page 71
Problem IX. Problem 2.45, Brown/Vranesic textbook, page 71
Problem X. Problem 2.46, Brown/Vranesic textbook, page 71