EECE 277
FPGA Design

Spring 2005, Dr. William H. Robinson

Homework Assignment Four

Due: Wednesday, March 16, 2005 at the beginning of class

Problem I. Problem 5.16, Brown/Vranesic textbook, page 311
Problem II. Problem 5.17, Brown/Vranesic textbook, page 311
Problem III. Problem 6.4, Brown/Vranesic textbook, page 372
Problem IV. Problem 6.16, Brown/Vranesic textbook, page 374
Problem V. Problem 6.18, Brown/Vranesic textbook, page 375
Problem VI. Problem 6.20, Brown/Vranesic textbook, page 375
Problem VII. Problem 6.26, Brown/Vranesic textbook, page 375
Problem VIII. Problem 6.31, Brown/Vranesic textbook, page 376
Complete this problem both schematically (with muxes) and in VHDL.