FPGA Design
EECE 277

In-Class Laboratory Day

Dr. William H. Robinson
February 2, 2005

http://eecs.vanderbilt.edu/courses/eece277/

Topics

“Procrastination is the thief of time.”
– Edward Young

“You may delay, but time will not.”
– Benjamin Franklin

“The best way to get something done is to begin.”
– Author Unknown

• Administrative stuff
  – “Read” Appendix A from your textbook (VHDL Reference)
  – Homework Assignment #2 (due Wednesday, February 2)
  – Laboratory Assignment #1 (due Monday, February 7)
  – Philippe Adell will present during Friday’s lecture